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11-14-06  
PATENT APPLICATION  
10/826,959

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

in re Application of:

James D. Ballew et al.

Serial No.:

10/826,959

Filing Date:

April 15, 2004

Group Art Unit:

2113

Confirmation No.:

1484

Title:

System and Method for Detecting and Managing  
HPC Node Failure

**Mail Stop: Amendments**

Commissioner for Patents

P.O. Box 1450

Alexandria, Virginia 22313-1450

Dear Sir:

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I hereby certify that this communication is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" under 37 C.F.R. § 1.10 on the date indicated below and is addressed to The Commissioner For Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

*Willie Jiles*

Willie Jiles

Date: November 13, 2006

**INFORMATION DISCLOSURE STATEMENT**

Applicants respectfully request, pursuant to 37 C.F.R. §§1.56, 1.97, and 1.98, that the documents listed on the attached Form PTO-1449 be considered and cited in the examination of the above-identified application.

Pursuant to the changes to C.F.R. §1.98(a)(2), copies of U.S. patents and U.S. patent application publications have not been provided. References other than U.S. patents and U.S. patent application publications are enclosed for the convenience of the Examiner.

Pursuant to 37 C.F.R. §§1.97(g) and (h), Applicants make no representation that these documents qualify as prior art or that these documents are material to patentability of the present application or that a search has been made.

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PATENT APPLICATION  
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**REMARKS**

The first Office Action has been received by the Applicants in the above identified patent application. Pursuant to 37 CFR § 1.97(c)(2), please charge the filing fee of \$180.00 to Deposit Account No. 02-0384 of Baker Botts. The Commissioner is also authorized to charge any other fees or credit any overpayments to Deposit Account No. 02-0384 of BAKER BOTTS L.L.P.

Respectfully submitted,  
BAKER BOTTS L.L.P.  
Attorneys for Applicants



Travis W. Thomas  
Reg. No. 48,667

Date: November 13, 2006

**Correspondence Address:**

**Customer Number 45507**

PTO-1449 <b>Information Disclosure Citation In an Application</b>		Application No. <b>10/826,959</b>	Applicant(s) <b>James D. Ballew et al.</b>	
		Docket Number <b>064747.1015</b>	Group Art Unit <b>2113</b>	Filing Date <b>April 15, 2004</b>

**U.S. PATENT DOCUMENTS**

	<b>DOCUMENT NO.</b>	<b>DATE</b>	<b>NAME</b>	<b>CLASS</b>	<b>SUBCLASS</b>	<b>FILING DATE</b>
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**FOREIGN PATENT DOCUMENTS**

<b>DOCUMENT NO.</b>	<b>DATE</b>	<b>COUNTRY</b>	<b>CLASS</b>	<b>SUBCLASS</b>	<b>TRANSLATION</b>
					<b>YES</b>

**NON-PATENT DOCUMENTS**

	<b>DOCUMENT (Including Author, Title, Source, and Pertinent Pages)</b>	<b>DATE</b>
	Communication from the European Patent Office, European Search Report for Application No. PCT/US2005/012500 and Written Opinion of the International Search Authority, mailed 08/01/2005, 12 pages	08/01/2005
<b>B</b>	Communication from the European Patent Office, European Search Report for Application No. PCT/US2005/012489 and Written Opinion of the International Search Authority, mailed 11/18/2005, 14 pages	11/18/2005
<b>C</b>	Unknown, "HP AlphaServer SC User Guide," Internet Article, Online!, July 12, 2004, retrieved from the internet: URL: <a href="http://web1.quadrics.com/onlinedocs/A1phaServer/Eagle/html/AlphaServerUserGuide/">http://web1.quadrics.com/onlinedocs/A1phaServer/Eagle/html/AlphaServerUserGuide/</a> retrieved on 07/19/2005	07/12/2004
<b>D</b>	Choo et al., "An Efficient Submesh Allocation Scheme for 3D Torus Multicolor Systems," Parallel Algorithms/Architecture Synthesis, 1997, Proceedings, Second Aizu International Symposium Aizu-Wakamatsu, Japan, March 17-21, 1997	03/17/1997 - 03/21/1997
<b>E</b>	Unknown, "Message Passing Interface (MPI)," Internet Article, Online!, December 23, 2003, pages 1-33, retrieved from the internet: URL: <a href="http://web.archive.org/web/20040102194825/http://www.llnl.gov/computing/tutorial s/mpl/">http://web.archive.org/web/20040102194825/http://www.llnl.gov/computing/tutorial s/mpl/</a> , retrieved on 07/18/2005, pages	12/23/2003
<b>F</b>	Panagiotis, Christias et al., "inetd - Internet Services Daemon," pages 1-4, 1994 Man-cgi 1.15S, 1995 Modified for Solaris 2.3	1994
<b>G</b>	Unknown, "Cisco Local Director Configuration and Command Reference Guide," Internet Article, Online, April 4, 2001	04/04/2001
<b>H</b>	Moore et al., "Managing Mixed-Use Clusters with Clusters-onDemand," Internet Article, November 2002	November 2002
<b>I</b>	Di Santo et al., "Kernel Implementation of Locality-Aware Dispatching Techniques for Web Server Clusters," Cluster Computing, 2003 Proceedings, Pages 154-162	12/01/2003
<b>J</b>	Ross et al., "3.3 Connectionless Transport," February 22, 2001, pages 1-4, retrieved on 11/01/2005	11/02/2005
<b>K</b>	Davidson et al., Patent Application Serial No. 10/825,345, entitled, "System and Method for Computer Cluster Virtualization Using Dynamic Boot Images and Virtual Disk, 47 pages of specification, 10 pages of drawings, Attorney Docket No. 064747.1011	04/15/2004
<b>L</b>	Davidson, Patent Application Serial No. 10/991,598, entitled, "Scheduling in a High Performance Computing (HPC) System", 111 pages specification, claims and abstract, 11 pages of drawings, Attorney Docket No. 064747.1048	11/17/2004
<b>M</b>	Davidson, Patent Application Serial No. 10/991,994, entitled, "On-Demand Instantiation in a High-Performance Computing (HPC) System", 110 pages specification, claims and abstract, 12 pages of drawings, Attorney Docket No. 064747.1049	11/17/2004

<b>EXAMINER</b>	<b>DATE CONSIDERED</b>

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.